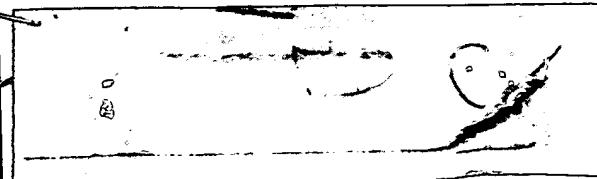


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228	214	
	Class	Subclass
		ISSUE CLASSIFICATION



PATENT NUMBER

6585150



6585150

U.S. UTILITY PATENT APPLICATION

O.I.P.E.

PATENT DATE

JUL 01 2003

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MKT Am 2 Q.A. UPS
SCANNED

Best Available Copy

APPLICATION NO. 09/687524	CONT/PRIOR	CLASS 228	SUBCLASS 214	ART UNIT 1725	EXAMINER Johnson
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PLICANTS William Bernier
Donald Henderson
James Spalik
Isabelle Paquin

Solder protective coating and fluxless joining of flip chip devices
on laminates with plated solder

TITLE

PTO-2040
12/99

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	<u>Joseph J. Jr.</u> (Assistant Examiner)			15	1
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	<u>TOM DUNN</u> TOM DUNN SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 1700 (Primary Examiner)			1/27/03	1/27/03
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	<u>DAN</u> (Legal Instruments Examiner)			1/25/03 (Date) 3/4/03 (Date)	\$1300 4/21/03 ISSUE BATCH NUMBER
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ISSUE FREE IN FILE (LABEL AREA)

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